PATENTS 112055-0073U 17732-67070.00



## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of: Pravas Pradhan et al.	)	
Serial No.: 10/720,857	)	Examiner: Not yet assigned
Filed: November 24, 2003	)	Art Unit: 2816
For: CURRENT TRANSFER LOGIC	) ) )	·
	,	Cesari and McKenna, LLP 88 Black Falcon Avenue Boston, MA 02210 March 9 2004

## **CERTIFICATE OF MAILING**

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In keeping with the duty of candor and good faith owed to the Patent and Trademark Office, Applicants wish to bring to the Examiner's attention the references listed on the accompanying form PTO-1449. A copy of each listed reference is enclosed.

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To the extent required by 37 C.F.R. §1.98(a)(3), Applicants have described what they consider to be the relevance of any foreign-language reference. The Office may find additionally relevant material in these or other references.

Please charge any additional fee occasioned by this paper to our Deposit Account No. 03-1237.

Respectfully submitted,

Edwin H. Paul

Reg. No. 31,405

CESARI AND MCKENNA, LLP

88 Black Falcon Avenue Boston, MA 02210-2414

(617) 951-2500

PTO/SB/08A (08-00)

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Denada	Complete if Known	
Application Number	10/720,857	·
Filing Date	November 24, 2003	
First Named Inventor	Pravas Pradhan et al.	
Group Art Unit	2816	
Examiner Name	Not yet assigned	
Attorney Docket Number	112055-0073U	

U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No. <sup>1</sup>	l Number	ocument d Code <sup>2</sup> (nown)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	5,801,564		Gasparik	09-01-1998	
	2.	5,811,984		Long et al.	09-22-1998	
	3.	6,236,269	B1	Hojabri	05-22-2001	
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	5.	6,320,417	B1	Kirsch et al.	11-20-2001	
	6.	6,476,642	B1	Morano	11-05-2002	
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r Initials*	Cite No.1	Office <sup>3</sup>	Number⁴	Kind Code <sup>5</sup> (if known)	Applicant of Cited Document	Cited Document MM- DD-YYYY	Where Relevant Passages or Relevant Figures Appear	
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Substitute for form 1449A/PTO Complete if Known **Application Number** 10/720,857 INFORMATION DISCLOSURE Filing Date November 24, 2003 STATEMENT BY APPLICANT First Named Inventor Pravas Pradhan et al. Group Art Unit 2816 (use as many sheets as necessary) **Examiner Name** Not yet assigned Sheet 112055-0073U **Attorney Docket Number** 

OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS						
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
	7.	SEEVINCK, Evert, Current-Mode Techniques for High-Speed VLSI Circuits with to Current Sense Amplifier for CMOS SRAM's, IEEE Journal of Solid-State Circuits, Vol. 26, No. 4, April 1991				
	8.	SIM, Jae-Yoon, A 1-Gb/s Bidirectional I/O Buffer Using the Current-Mode Scheme, IEEE Journal of Solid-State Circuits, Vol. 34, No. 4, April 1999				

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